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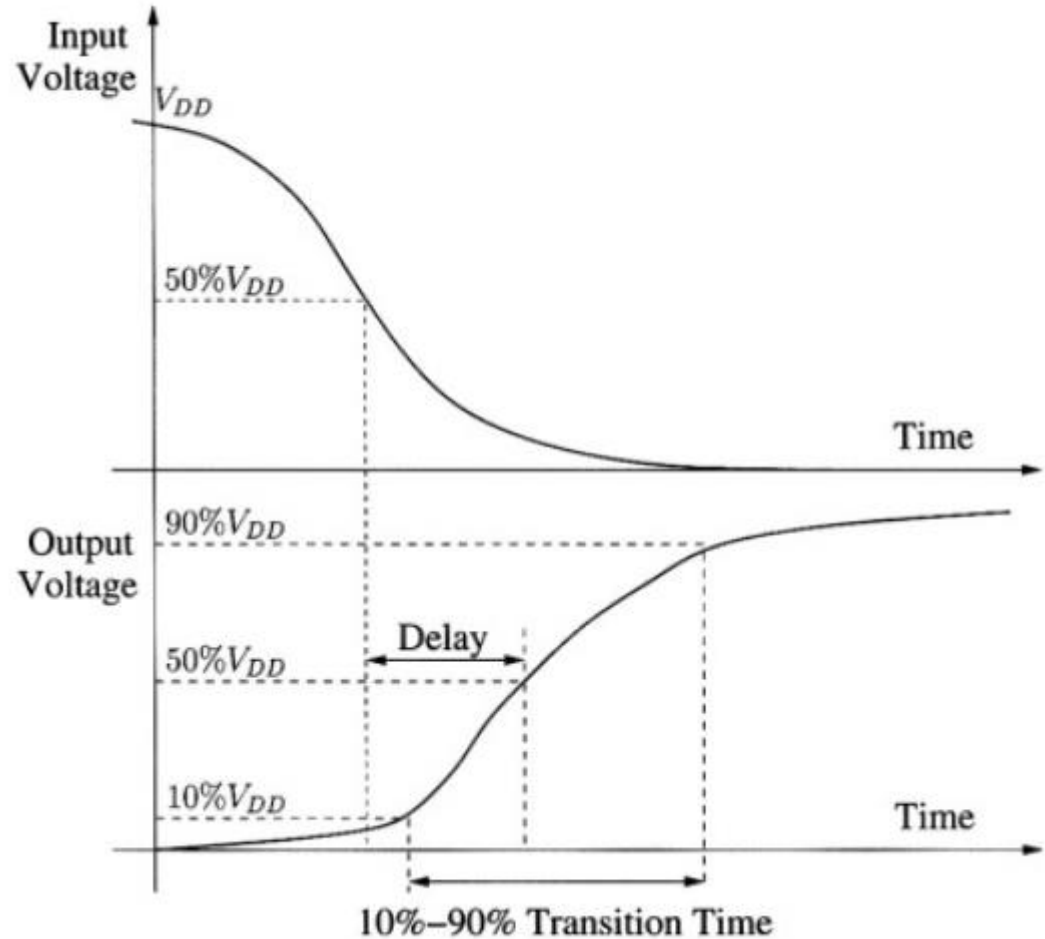
# Asymptotic Waveform Evaluation for Timing Analysis

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# Delay

- The most fundamental way to compute delay is to develop a physical model of the circuit of interest, write a differential equation describing the output voltage as a function of input voltage and time, and solve the equation.
- The solution of the differential equation is called the **transient response**, and the **delay** is the time when the output reaches  $V_{DD} / 2$

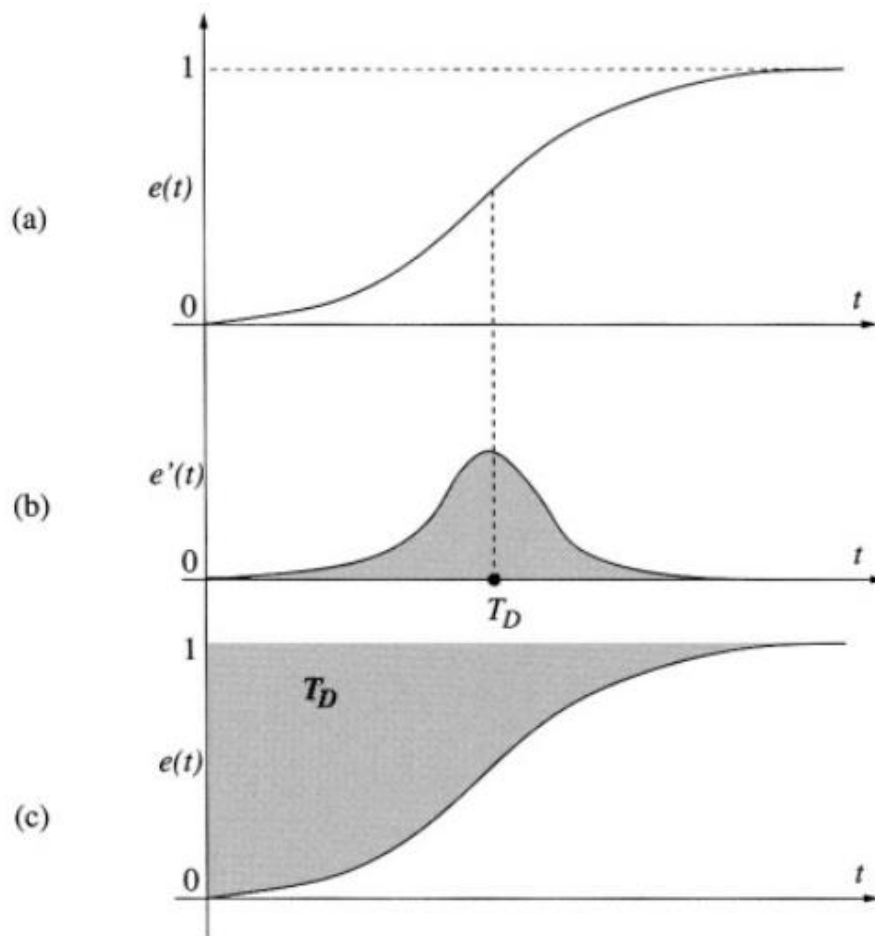


# Elmore delay

The step response  $e(t)$  at some node in a circuit (and its derivative  $e'(t)$ ) is shown in left picture.

The time coordinate of the centroid of  $e'(t)$  is the Elmore delay  $T_D$

$$\begin{aligned} T_D &= \int_0^{\infty} t e'(t) dt \\ &= \int_0^{\infty} [1 - e(t)] dt \end{aligned}$$

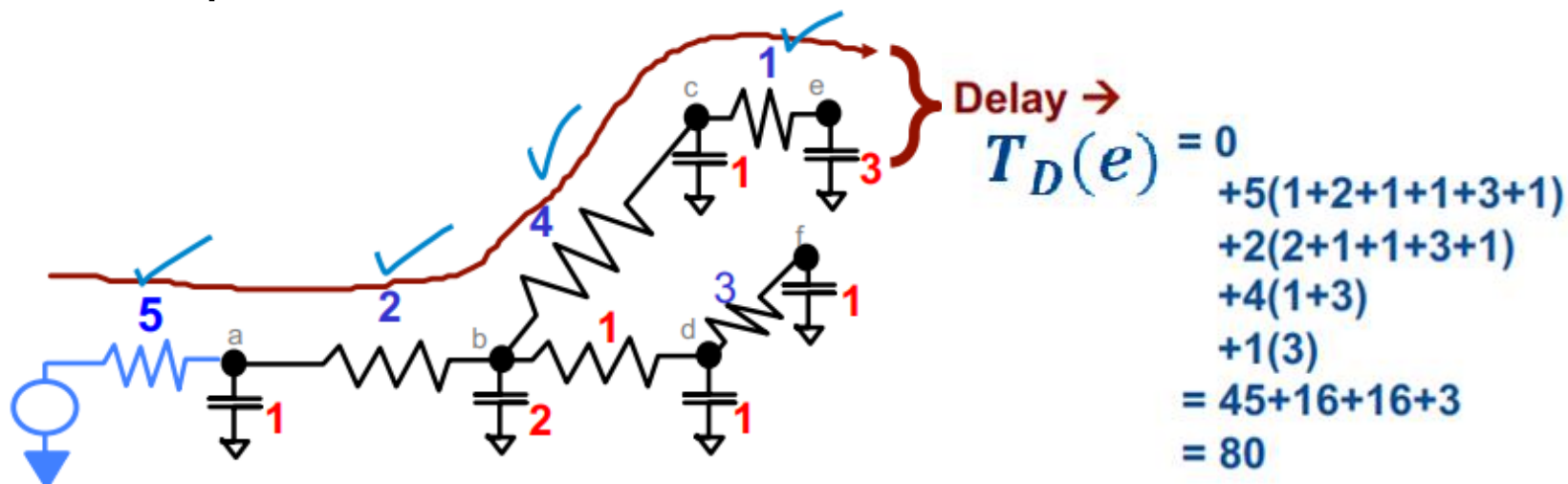


# Elmore delay

The Elmore delay to node  $n_i$  in the RC tree is:

$$T_{Di} = \sum_{j \in P_i} R_j \sum_{k \in \text{downstream}(j)} C_k$$

Example:



# Asymptotic Waveform Evaluation

## Moment of a transfer function

$$F(s) = \int_0^{\infty} f(t)e^{-st} dt \quad \leftarrow \text{The Laplace transform of a transfer function}$$

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$$\begin{aligned} F(s) &= \int_0^{\infty} f(t) \left[ 1 - st + \frac{s^2 t^2}{2!} - \frac{s^3 t^3}{3!} + \dots \right] dt \quad \leftarrow \text{MacLaurin series expansion} \\ &= \int_0^{\infty} f(t) dt - s \int_0^{\infty} t f(t) dt + s^2 \int_0^{\infty} \frac{t^2}{2!} f(t) dt - s^3 \int_0^{\infty} \frac{t^3}{3!} f(t) dt + \dots \\ &= m_0 + m_1 s + m_2 s^2 + m_3 s^3 + \dots \end{aligned}$$

$$F(s) = m_0 + \sum_i^{\infty} m_i s^i$$

# Moment matching

## ※ Pade's approximation:

对于正整数 $m, n$ , 函数 $f(x)$ 在 $[m, n]$ 阶的帕德逼近为

$$R(x) = \frac{\sum_{j=0}^m a_j x^j}{1 + \sum_{k=1}^n b_k x^k} = \frac{a_0 + a_1 x + a_2 x^2 + \cdots + a_m x^m}{1 + b_1 x + b_2 x^2 + \cdots + b_n x^n}$$

且 $f(0)=R(0)$ ,  $f'(0)=R'(0)$ , 即  $f^n(0) = R^n(0)$

$$\text{Let } m_0 + m_1 s + m_2 s^2 + \cdots \equiv \frac{a_0 + a_1 s + \cdots + a_{q-1} s^{q-1}}{1 + b_1 s + \cdots + b_q s^q}$$

$$(m_0 + m_1 s + m_2 s^2 + \cdots) \cdot (1 + b_1 s + \cdots + b_q s^q) \equiv a_0 + a_1 s + \cdots + a_{q-1} s^{q-1}$$

$$m_0 + (m_1 + m_0 b_1)s + (m_2 + m_1 b_1)s^2 + \cdots \equiv a_0 + a_1 s + a_2 s^2 \cdots$$

$$s^0 : \quad a_0 = m_0$$

$$s^1 : \quad a_1 = m_1 + m_0 b_1$$

$$s^2 : \quad a_2 = m_2 + m_1 b_1 + m_0 b_2$$

$$\vdots$$

$$s^{q-1} : \quad a_{q-1} = m_{q-1} + m_{q-2} b_1 + \cdots + m_1 b_{q-2} + m_0 b_{q-1}$$

# MNA matrix

MNA matrix:  $(\mathbf{G} + s\mathbf{C})\mathbf{X} = \mathbf{E}$

$\mathbf{G} / \mathbf{C}$ : constant matrices (depend on the values of the RLC elements)

$\mathbf{X}$ : vector of unknowns

$\mathbf{E}$ : excitation vector

Represent  $\mathbf{X}$  in terms of its moments

$$\mathbf{X} = \mathbf{m}_0 + \mathbf{m}_1 s + \mathbf{m}_2 s^2 + \mathbf{m}_3 s^3 + \dots$$

When the excitation is  $\delta(t)$ , we have  $\mathbf{E} = \mathbf{E}_0$  in the  $s$  domain

$$(G + sC)(\mathbf{m}_0 + \mathbf{m}_1 s + \mathbf{m}_2 s^2 + \dots) = \mathbf{E}_0$$

# Moment computation

$$(G + sC)(\mathbf{m}_0 + \mathbf{m}_1 s + \mathbf{m}_2 s^2 + \cdots) = \mathbf{E}_0$$

$$G\mathbf{m}_0 + (G\mathbf{m}_1 + C\mathbf{m}_0)s + (G\mathbf{m}_2 + C\mathbf{m}_1)s^2 + \cdots = \mathbf{E}_0$$

$$G\mathbf{m}_0 = \mathbf{E}_0 \quad (i = 0)$$

The solution of Equation is identical to that of the original system when an impulse excitation is applied, and is set  $\mathbf{s}$  to zero.

$$G\mathbf{m}_i = -C\mathbf{m}_{i-1} \quad (\forall i \geq 1)$$

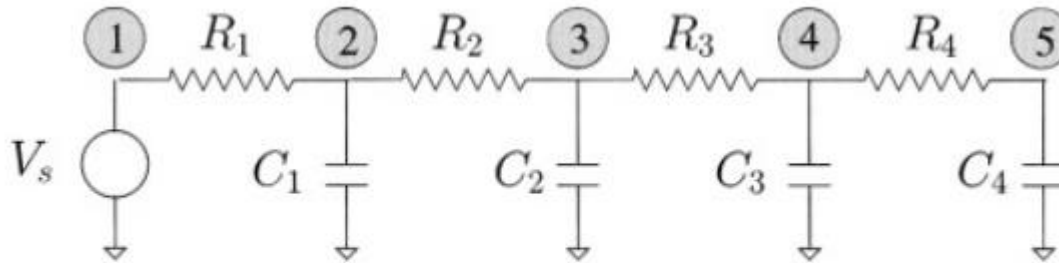
This equation corresponds to the original excitation is set to zero, and a new excitation of  $-C\mathbf{m}_{i-1}$  is applied instead. Implies that the original circuit is modified as follows:

1. All voltage sources are short-circuited and current sources open-circuited.
2. Each capacitor is replaced by a current source of value  $C\mathbf{m}_{i-1}(V_c)$ , where  $\mathbf{m}_{i-1}(V_c)$  is the  $(i-1)^{th}$  moment of the voltage  $V_c$  across the capacitor.
3. Each self or mutual inductance  $\mathbf{L}_{ij}$  is replaced by a voltage source of value  $\mathbf{L}_{ij}\mathbf{m}_{i-1}(I_j)$  on line  $i$ , where  $\mathbf{m}_{i-1}(I_j)$  is the  $(i-1)^{th}$  moment of the current through inductor  $j$ .



# Example circuit

## Moment calculation for a simple RC line



An example of an RC line.

Let  $R = 1\Omega, C = 1F$

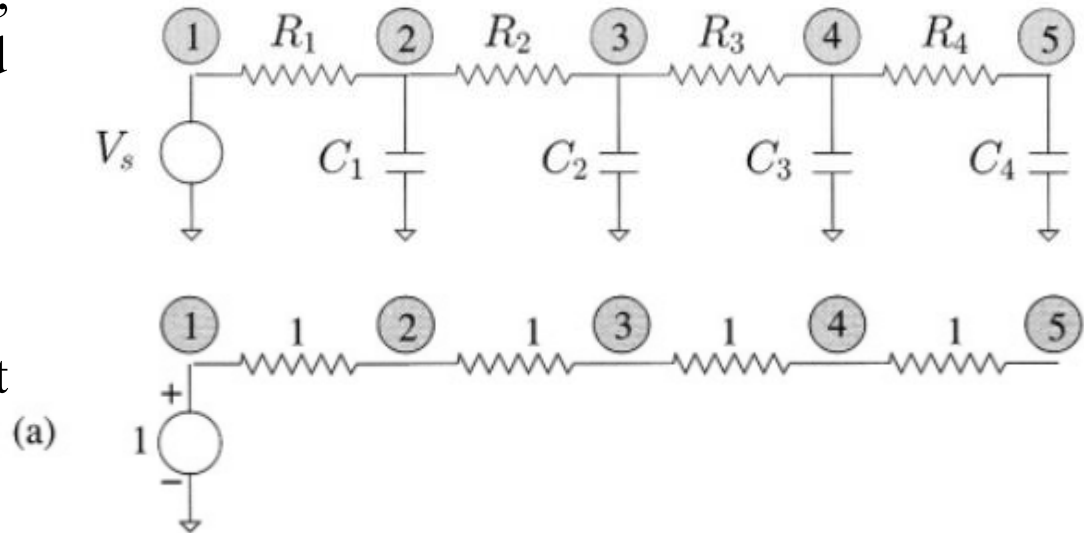
$$m_q = [v_1 \ v_2 \ v_3 \ v_4 \ v_5 \ i_v]^T$$

# Example Step.1

- Finding  $m_0$  i.e.  $Gm_0 = E_0$  ( $i = 0$ )

All capacitors are open-circuited, and the voltage source is replaced by a delta function in the time domain. (corresponds to a unit source in the  $s$  domain)

It is easily verified that no current can flow in the circuit.



Therefore, the voltage at each node is 1.

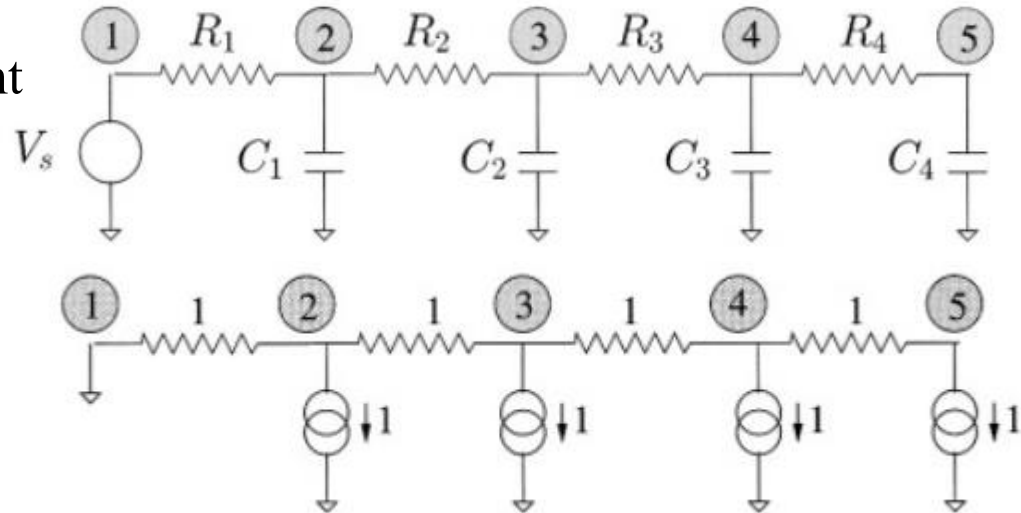
$$\mathbf{m}_0 = \begin{bmatrix} 1 & 1 & 1 & 1 & 1 & 0 \end{bmatrix}^T$$

# Example Step.2

- Finding  $\mathbf{m}_1$  i.e.  $\mathbf{G}\mathbf{m}_i = -\mathbf{C}\mathbf{m}_{i-1} \quad (\forall i \geq 1)$

Capacitors is now replaced by a current source of value  $\mathbf{C}_i \mathbf{m}_0(\mathbf{V}_i)$

The current in each branch can be calculated as the **sum of all downstream currents**

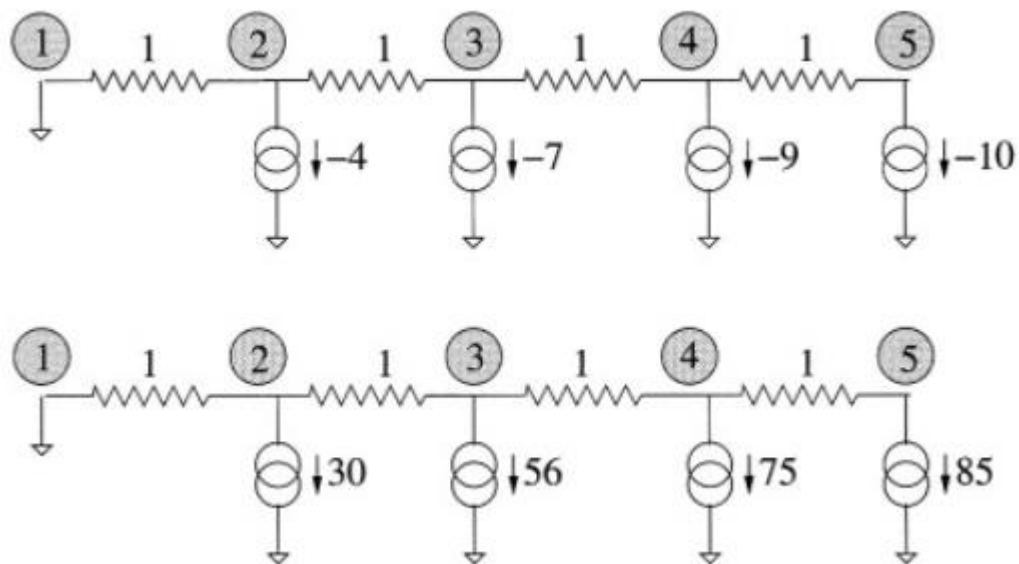


So the voltage at each node:

$$\mathbf{m}_1 = [ 0 \quad -4 \quad -7 \quad -9 \quad -10 \quad -4 ]^T$$

# Example Step.3

- Finding  $m_2, m_3$



$$\mathbf{m}_2 = [0 \quad 30 \quad 56 \quad 75 \quad 85 \quad 30]^T$$

$$\mathbf{m}_3 = [0 \quad -246 \quad -462 \quad -622 \quad -707 \quad -246]^T$$

# Example Step.4

	$m_0$	$m_1$	$m_2$	$m_3$
$v_1$	1	0	0	0
$v_2$	1	-4	30	-246
$v_3$	1	-7	56	-462
$v_4$	1	-9	75	-622
$v_5$	1	-10	85	-707
$i_v$	0	-4	30	-246



✱ Pade's approximation:

$$s^0: a_0 = m_0$$

$$s^1: a_1 = m_1 + m_0 b_1$$

$$s^2: a_2 = m_2 + m_1 b_1 + m_0 b_2$$

$$s^3: a_3 = m_3 + m_2 b_1 + m_1 b_2 + m_0 b_3$$



$$a_0 = 1$$

$$a_1 = b_1 - 10$$

$$0 = b_2 - 10b_1 + 85$$

$$0 = -10b_2 + 85b_1 - 707$$

# Example Step.4

$$a_0 = 1$$

$$a_1 = b_1 - 10$$

$$0 = b_2 - 10b_1 + 85$$

$$0 = -10b_2 + 85b_1 - 707$$



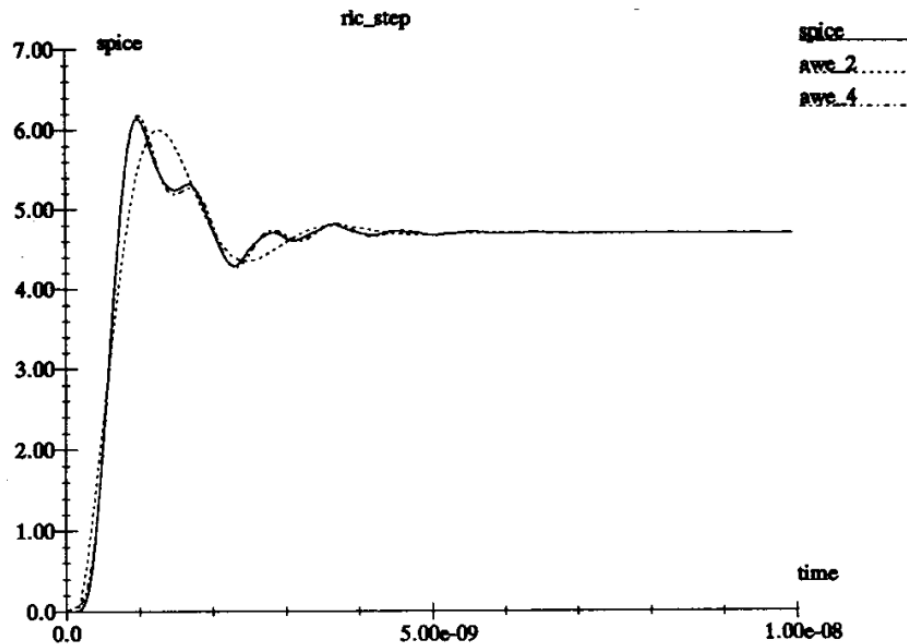
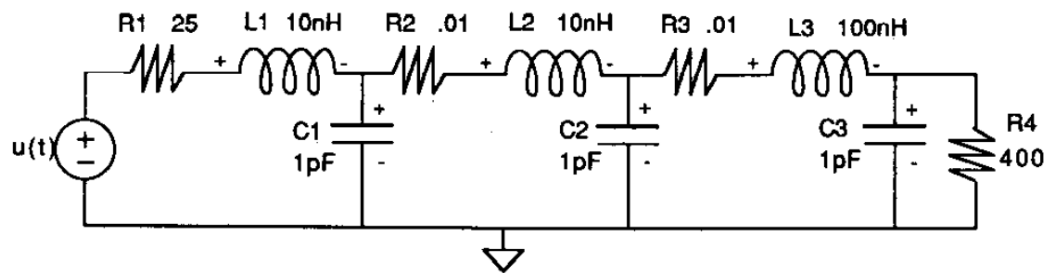
$a_0$	$a_1$	$b_1$	$b_2$	$m$
-1	0	0	0	-1
0	-1	1	0	10
0	0	-10	1	-85
0	0	85	-10	707



```
np.linalg.solve(mat,cons)
array([ 1.          , -0.46666667,  9.53333333, 10.33333333])

Latex(r"$H(s)=\frac{d\%.3fs}{1+\%.3fs+\%.3fs^2}$"%(a0,a1,b1,b2))
```

$$H(s) = \frac{1 - 0.467s}{1 + 9.533s + 10.333s^2}$$



L. W. Pillage and R. A. Rohrer, "Asymptotic waveform evaluation for timing analysis",

## AWE second- and fourth-order approximations for the step response of a RLC circuit

# Limitations of AWE

① The Pade approximations can be numerically unstable (higher moments becomes ill conditioned) .

- *Pade via Lanczos process* (improved numerical stability, but more computationally expensive)

[Time:  $O(nm \log n)$ , Space:  $O(m^2)$  for m Order]

② Can not guarantee passivity

- *Passive Reduced-order Interconnect Macromodeling Algorithm* (PRIMA).
- *Arnoldi algorithm*
- *Pole Analysis via Congruence Transformations* (PACT)

[Time:  $O(n^{1.5})$ , Space:  $O(n \log n)$ ]



# References

- S. Sapatnekar, *Timing*. New York: Springer-Verlag, 2004
- L. W. Pillage and R. A. Rohrer, "*Asymptotic waveform evaluation for timing analysis*", IEEE Trans. CAD, vol. 9, no. 4, pp. 352-366, Apr. 1990.
- L. T. Pillage, X. Huang and R. A. Rohrer, "*AWEsim: Asymptotic Waveform Evaluation for Timing Analysis*", 26th ACM/IEEE Design Automation Conference Proceedings, pp. 634-637, 1989.
- C. L. Ratzlaff and L. T. Pillage, "*RICE: Rapid interconnect circuit evaluation using AWE*," IEEE Trans. Comput.-Aided Design. Circuits Syst., vol. 13, no. 6, pp. 763-776, Jun. 1994.
- P. Feldmann and R. W. Freund, "*Efficient linear circuit analysis by Padé approximation via the Lanczos process*", Proc. EURO-DAC, pp. 170-175, 1994.
- K. J. Kerns, A. T. Yang, "*Stable and Efficient Reduction of Large, Multiport RC Networks by Pole Analysis via Congruence Transformations(PACT)*" IEEE. Trans. Computer-Aided Design, vol. 16, 1997.
- I. M. Elfadel and D. D. Ling, "*Zeros and passivity of Arnoldi-reduced-order models for interconnect networks*", Proc. 34th Design Automation Conf., pp. 28-33, 1997-June.
- L. Miguel Silveira, Mattan Kamen, Ibrahim Elfadel and Jacob White, "*A coordinate-transformed Arnoldi algorithm for generating guaranteed stable reduced-order models of RLC circuits*", Comput. Methods Appl. Mech., vol. 169, no. 3-4, pp. 377-390, Feb. 1999.
- S. Aaltonen, J. Roos, "*Simple reduced-order macromodels with PRIMA*", Electronics Circuits and Systems 2002. 9th International Conference on, vol. 1, pp. 367-370 vol.1, 2002.



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# 谢谢